

**Application Data Sheet****Application Information**

Application Type::	Regular
Subject Matter::	Utility
Title::	SEMICONDUCTOR STRUCTURE COMPRISING A HIGHLY DOPED CONDUCTIVE CHANNEL REGION AND METHOD FOR PRODUCING A SEMICONDUCTOR STRUCTURE
Attorney Docket Number::	S4-01P16203
Request for Early Publication::	No
Request for Non-Publication::	No
Suggested Drawing Figure::	1
Total Drawing Sheets::	7
Small Entity::	No
Petition included::	No
Secrecy Order in Patent Appl.?:	No

**Applicant Information**

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### **Domestic Priority Information**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	National Stage of	PCT/DE2003/000906	March 19, 2003

### **Assignee Information**

Assignee name:: Siced Electronics Development GmbH & Co. KG